

Wallpaper Group Atlas

Comments

Starting from a standard grid graph, this atlas presents for the standard wallpaper groups and suitably selected control points the resulting unit cell, the tile and a tiling in an array of $2 * 3$.

Procedures and patterns are based on routines, as provided by Igor Bakshee's Artlandia system. For more system details refer to the Artlandia manual.

This atlas of wallpaper groups was developed to support the pattern design process, which usually has to be carried out in a trial-and-error approach.

Besides the wallpaper group selection, the definition of control points takes paramount influence upon the resulting pattern. This becomes very obvious from these graphs.

This compilation is arranged in two sections, depending upon the number of required control points:

1. **two** points are required for the following wallpaper groups:

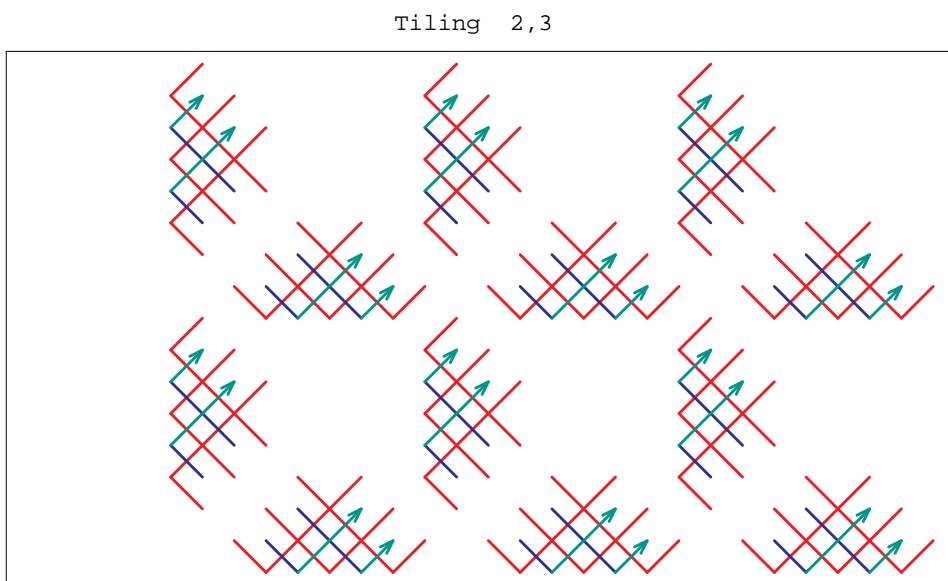
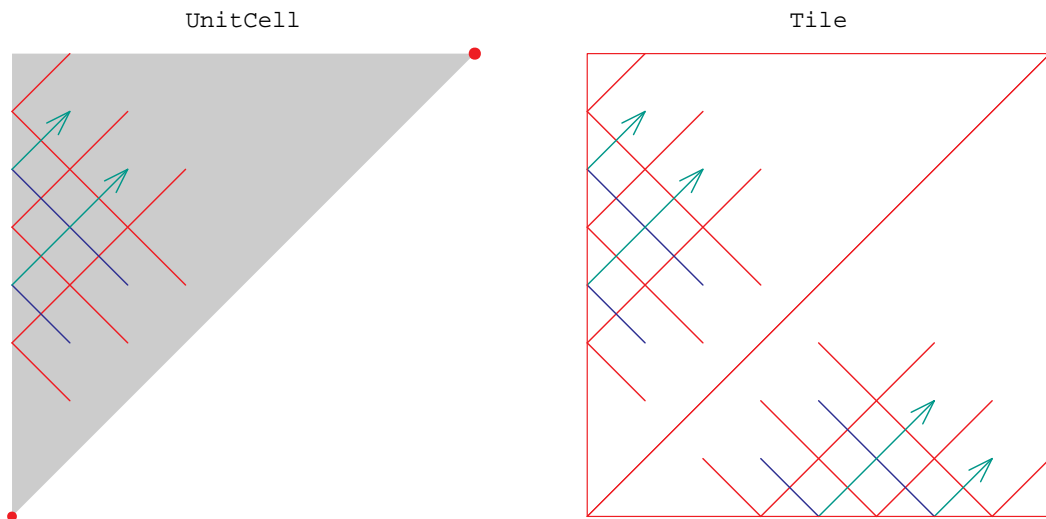
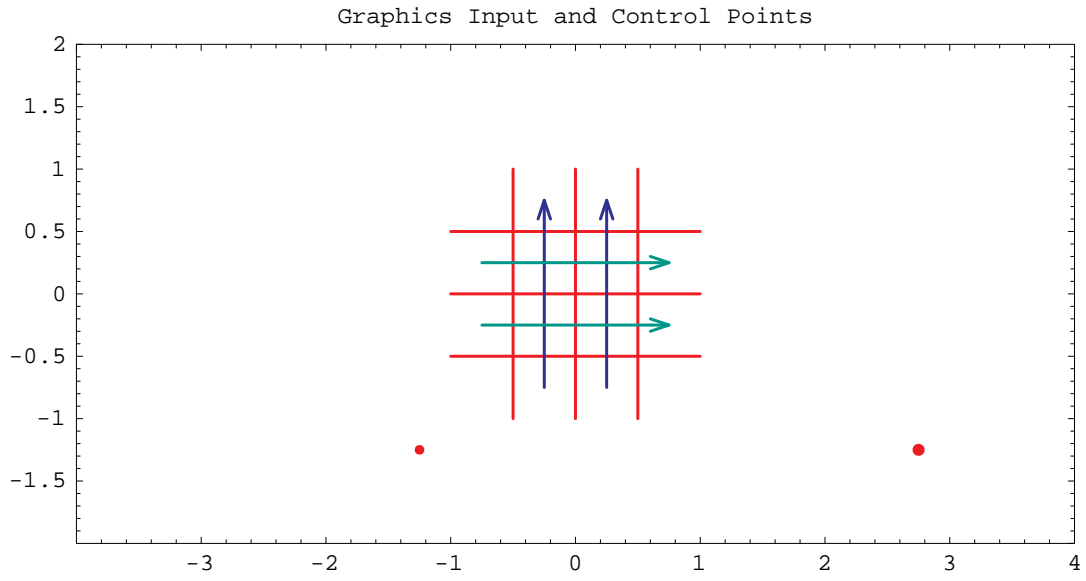
<i>cm</i>	<i>see page 3</i>
<i>cmm</i>	<i>see page 4</i>
<i>pmm</i>	<i>see page 5</i>
<i>p4</i>	<i>see page 6</i>
<i>p4g</i>	<i>see page 7</i>
<i>p4m</i>	<i>see page 8</i>
<i>p3</i>	<i>see page 9</i>
<i>p3m1</i>	<i>see page 10</i>
<i>p31m</i>	<i>see page 11</i>
<i>p6</i>	<i>see page 12</i>
<i>p6m</i>	<i>see page 13</i>

2. **three** points are required for the following wallpaper groups:

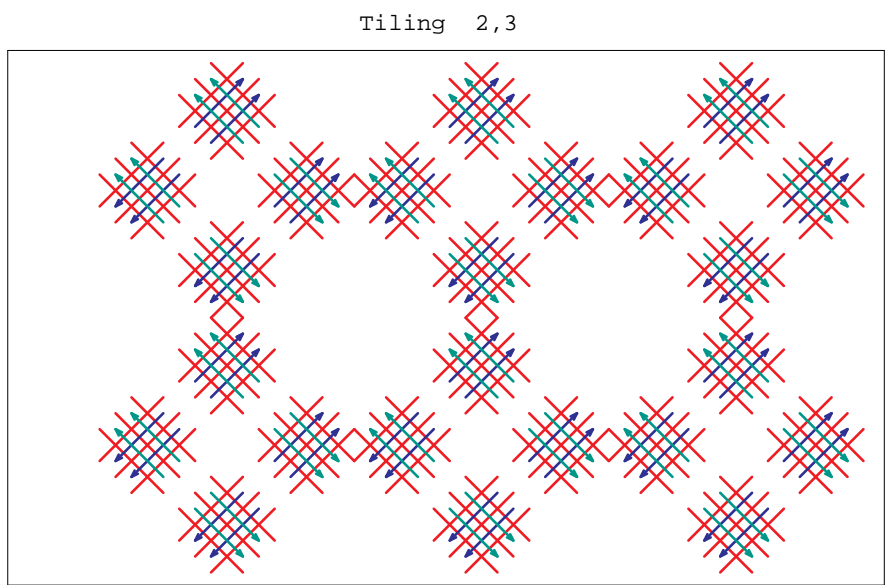
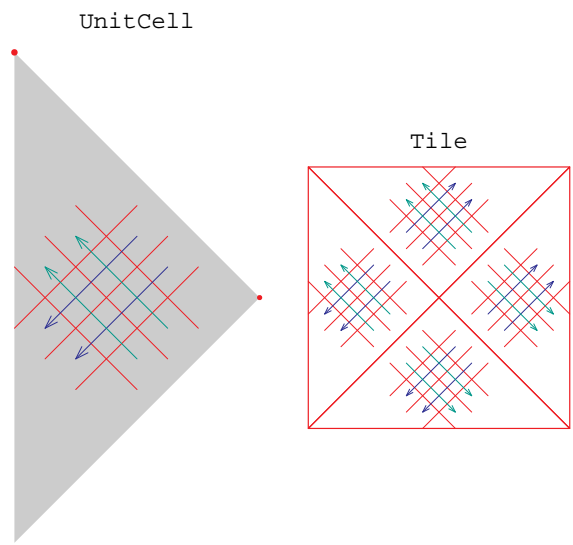
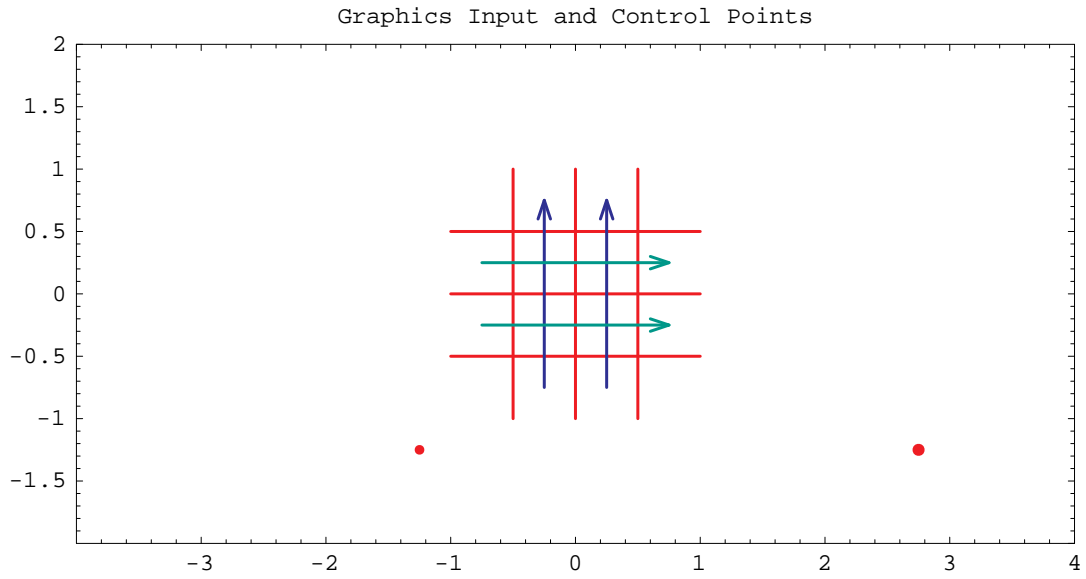
<i>p1</i>	<i>see page 14</i>
<i>pg</i>	<i>see page 15</i>
<i>pm</i>	<i>see page 16</i>
<i>pgg</i>	<i>see page 17</i>
<i>pmg</i>	<i>see page 18</i>
<i>p2</i>	<i>see page 19</i>

Some further editor information is provided on page 20.

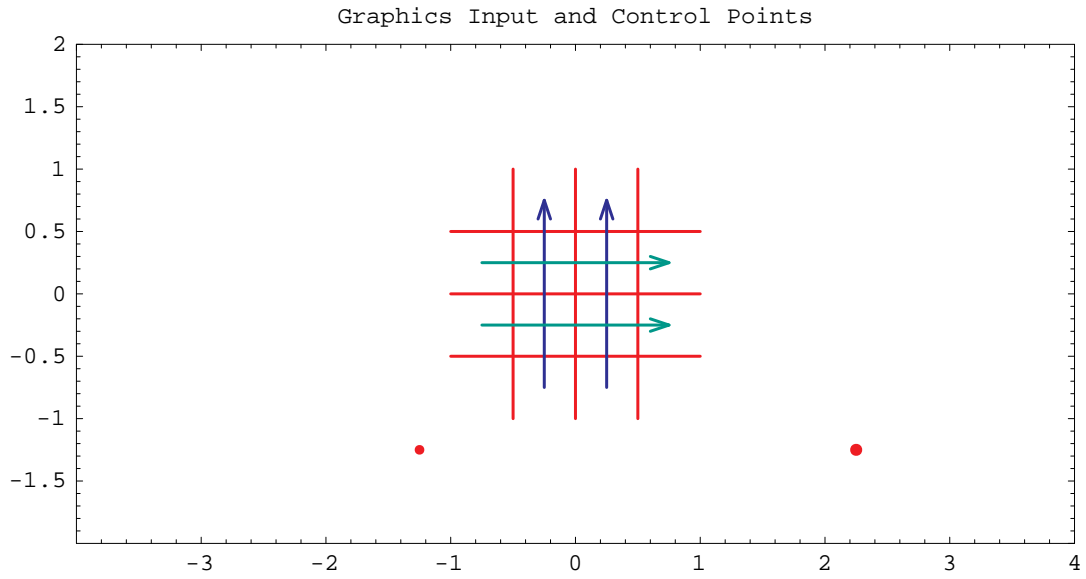
Wallpaper Group: cm



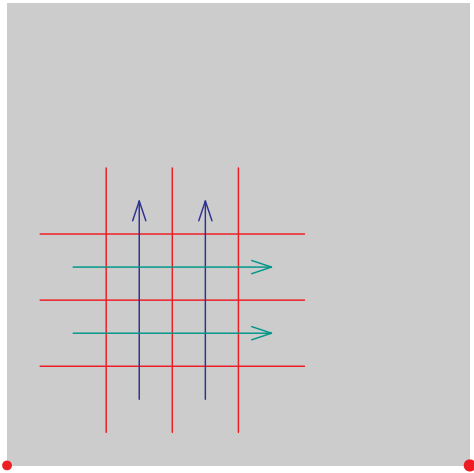
Wallpaper Group: **cmm**



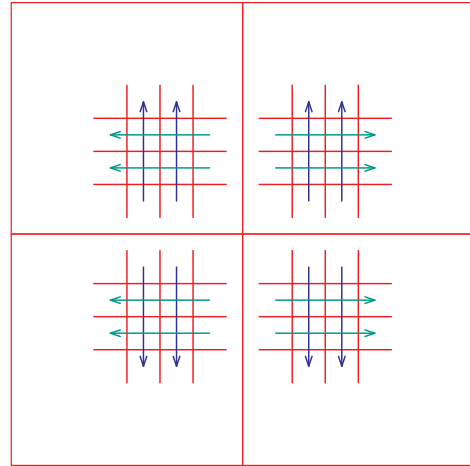
Wallpaper Group: pmm



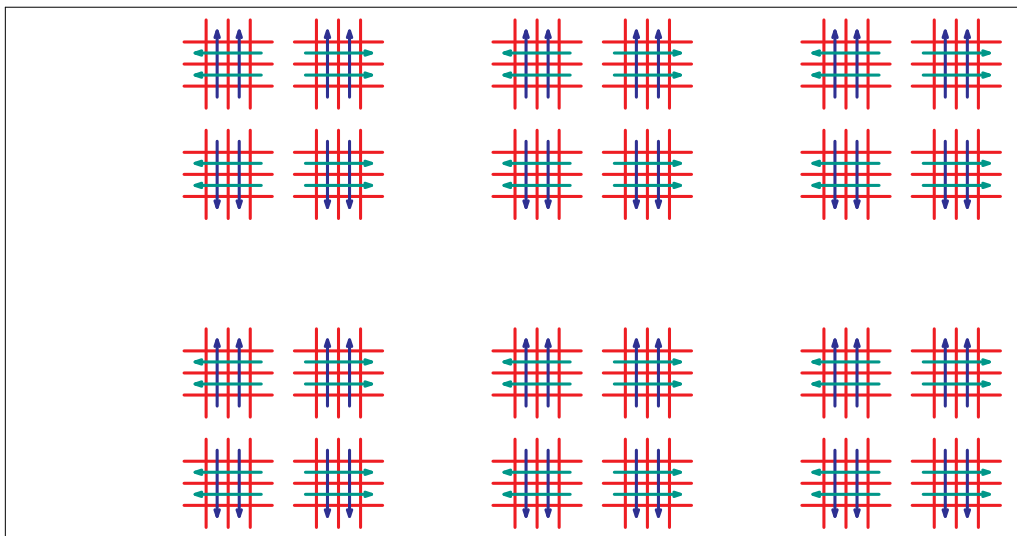
UnitCell



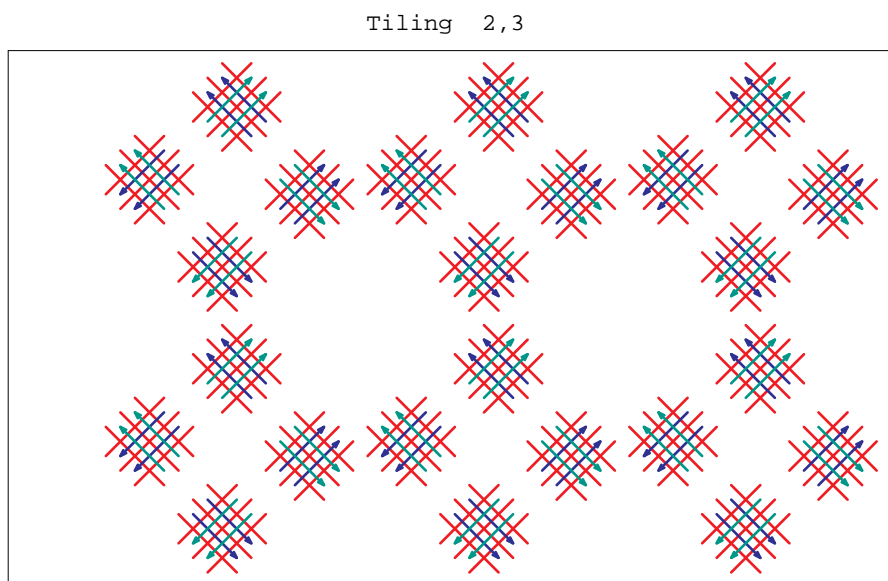
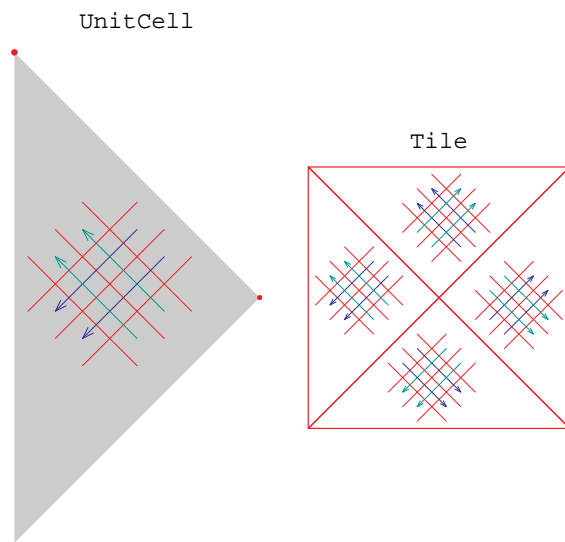
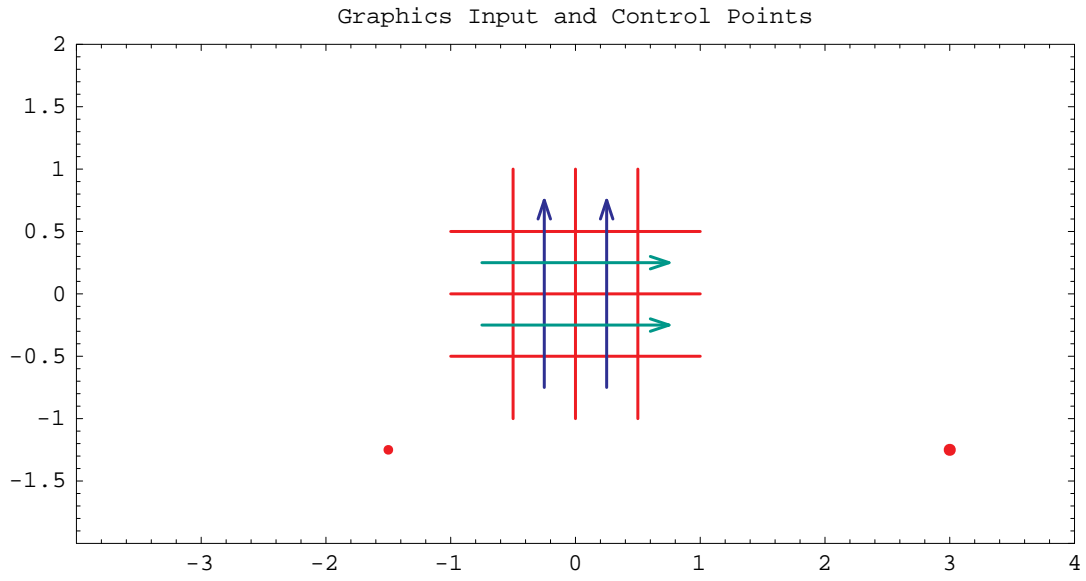
Tile



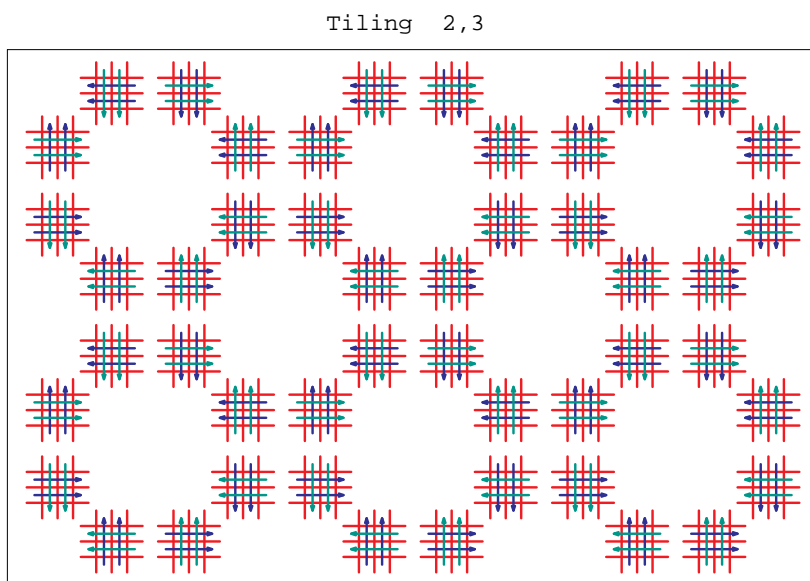
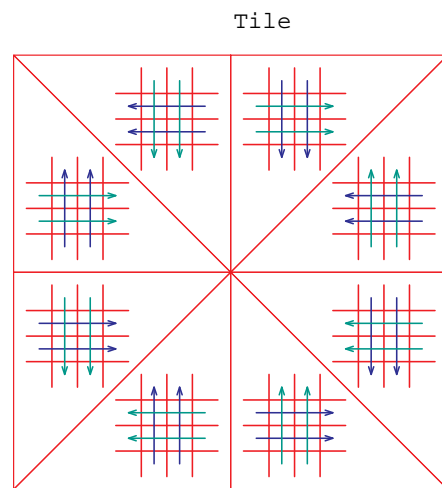
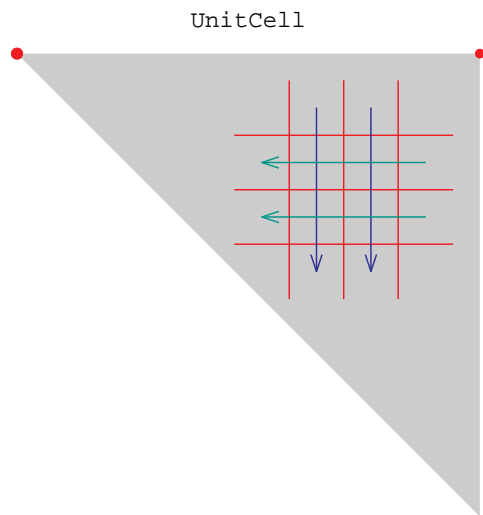
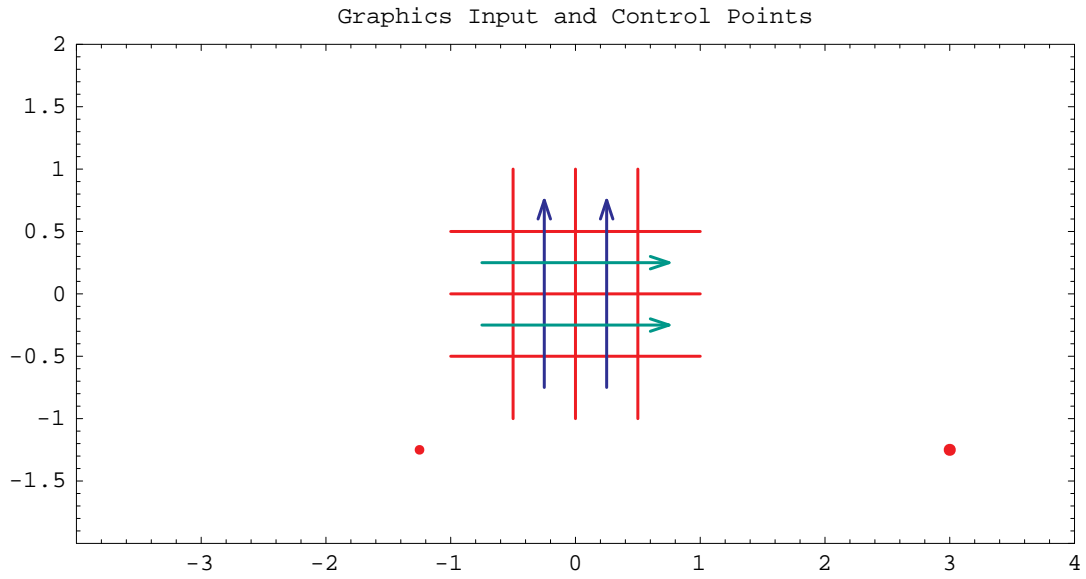
Tiling 2,3



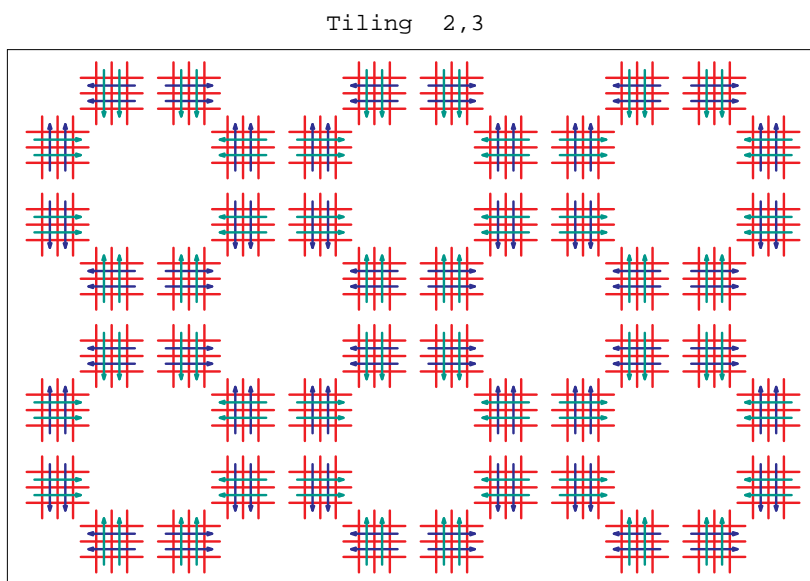
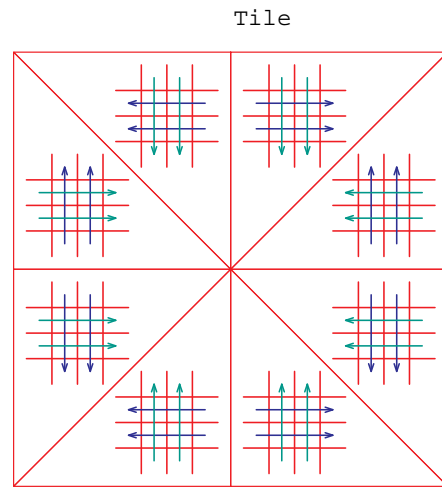
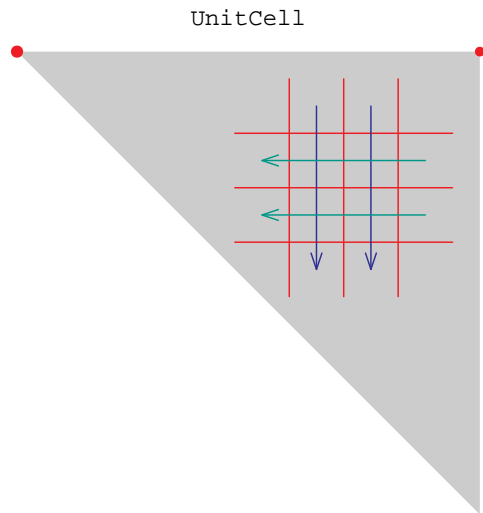
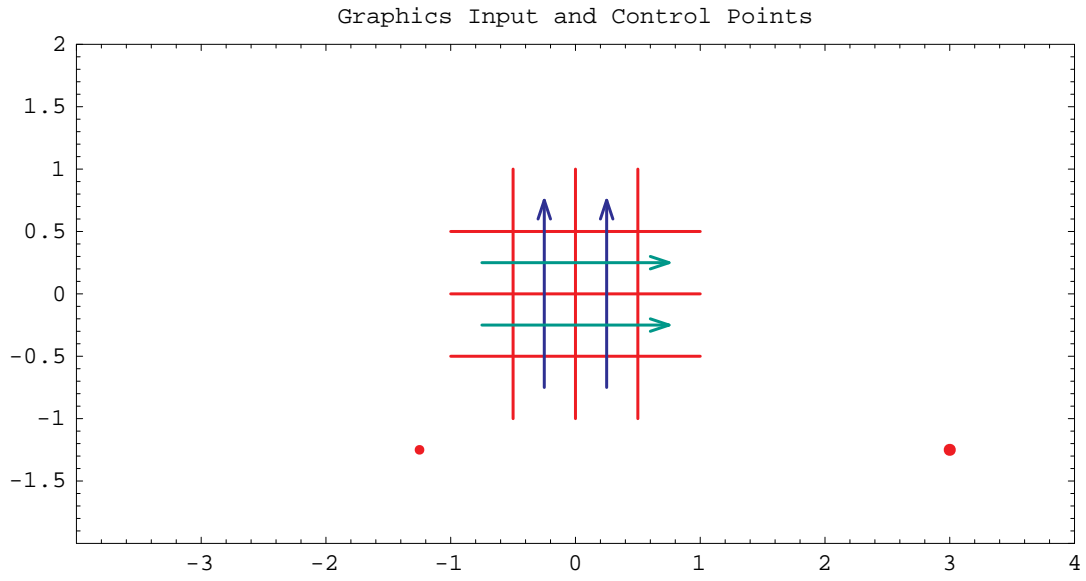
Wallpaper Group: p4



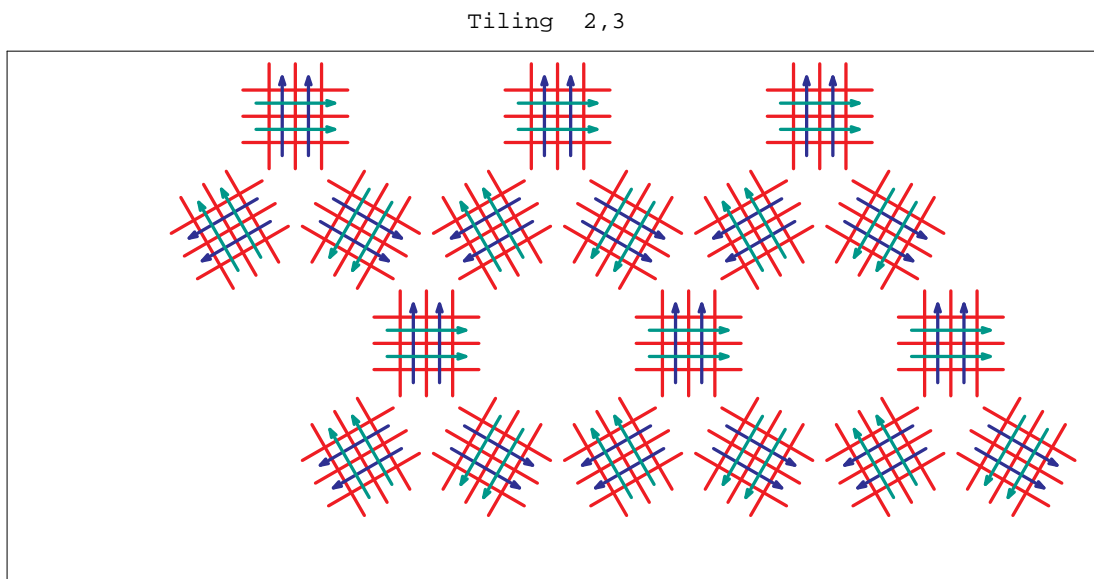
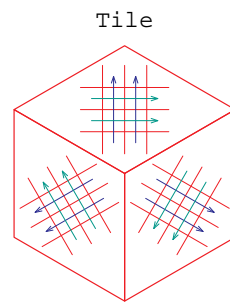
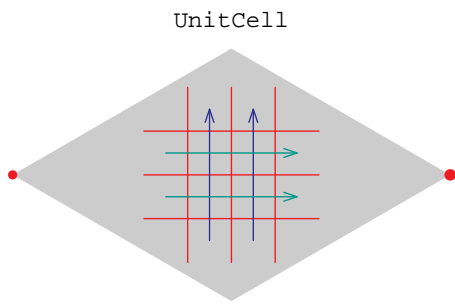
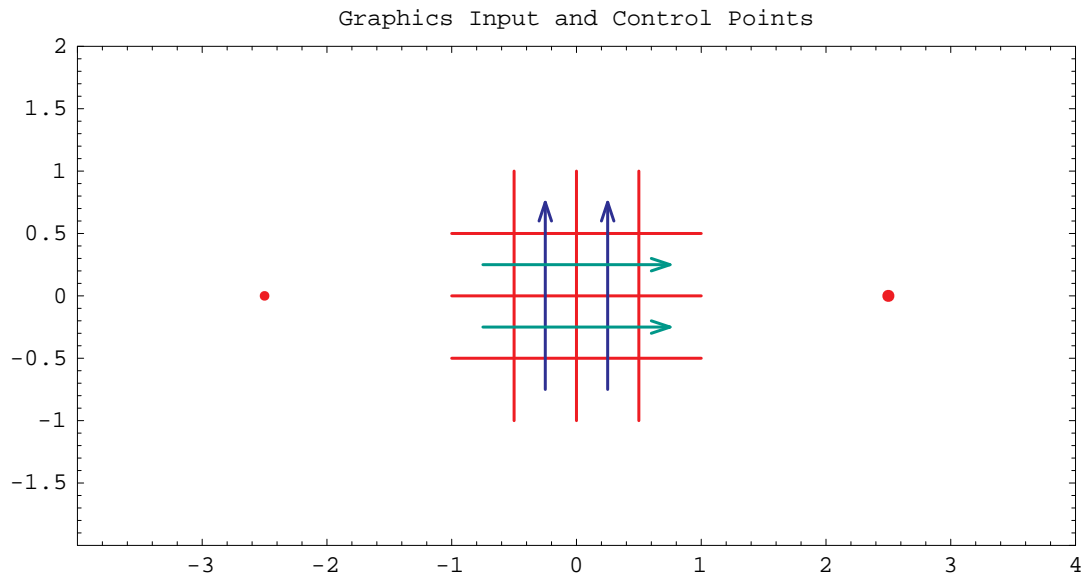
Wallpaper Group: p4g



Wallpaper Group: p4m

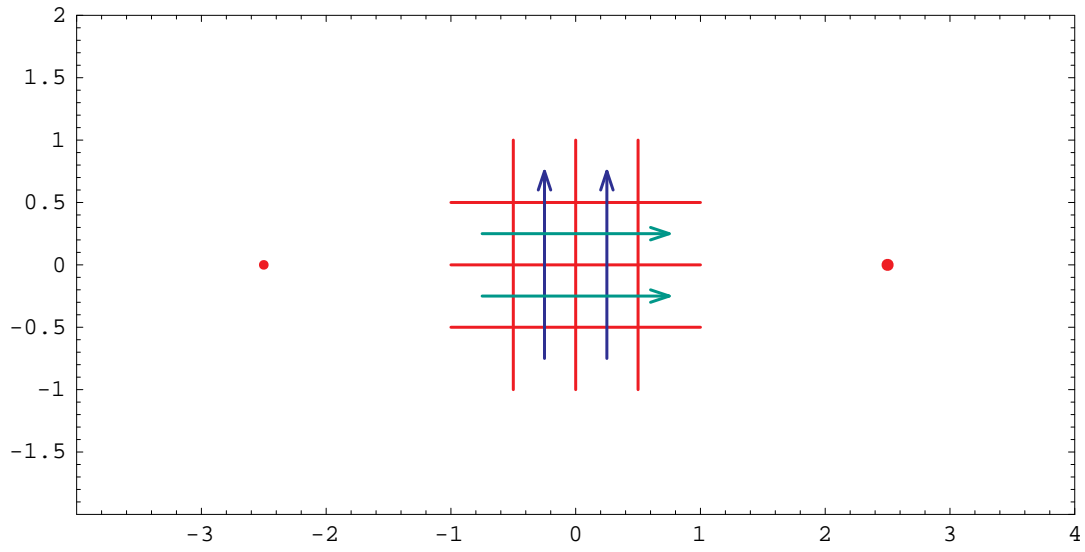


Wallpaper Group: p3



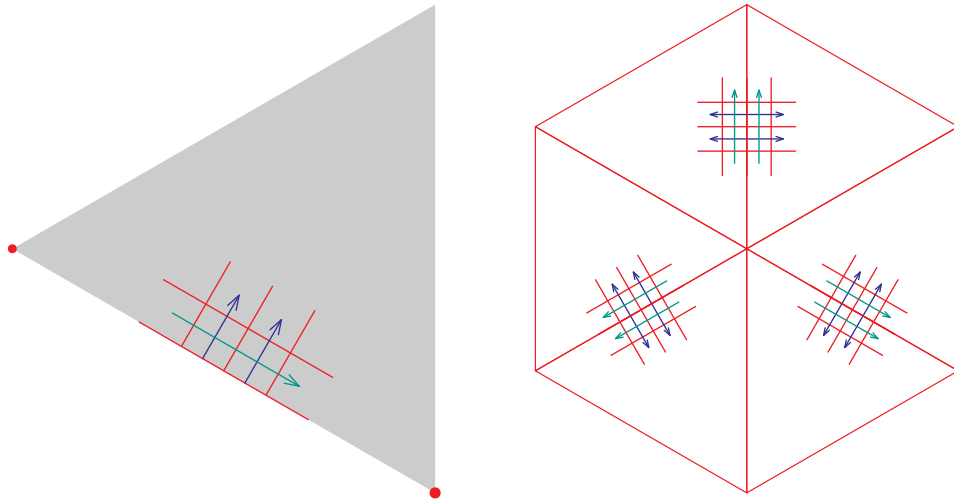
Wallpaper Group: p3m1

Graphics Input and Control Points

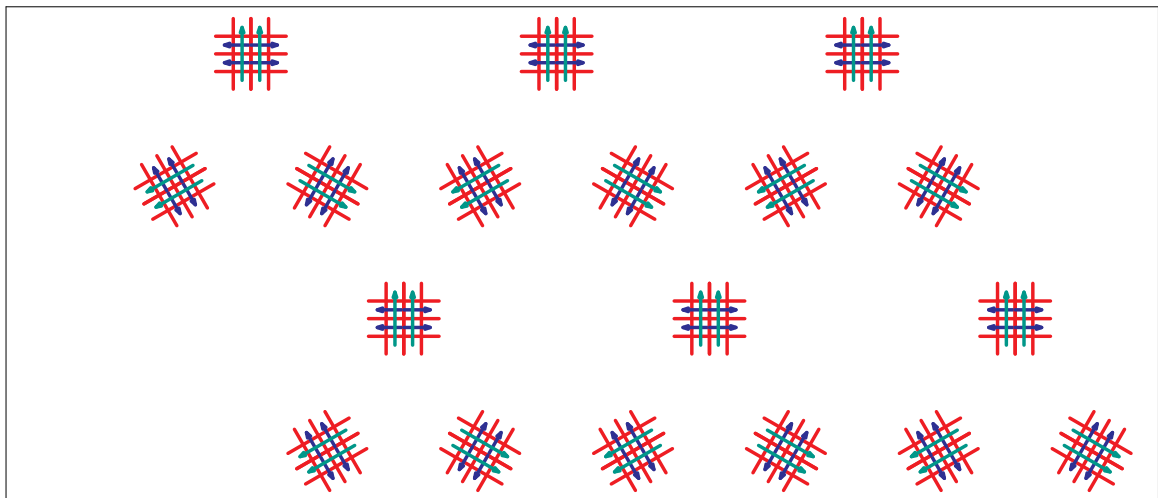


UnitCell

Tile

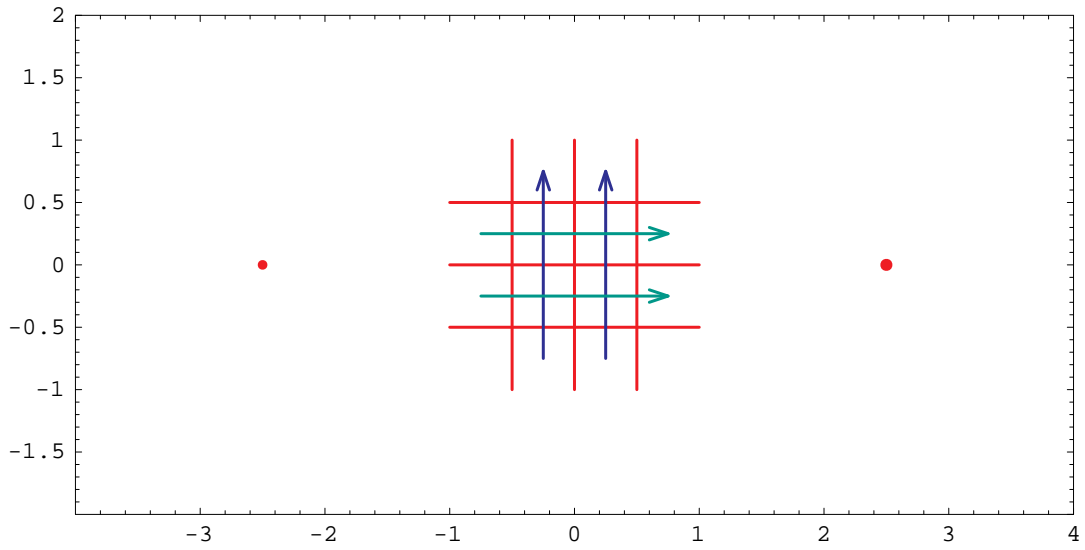


Tiling 2,3

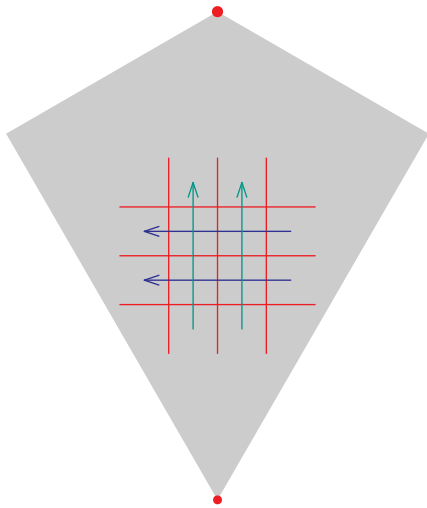


Wallpaper Group: p31m

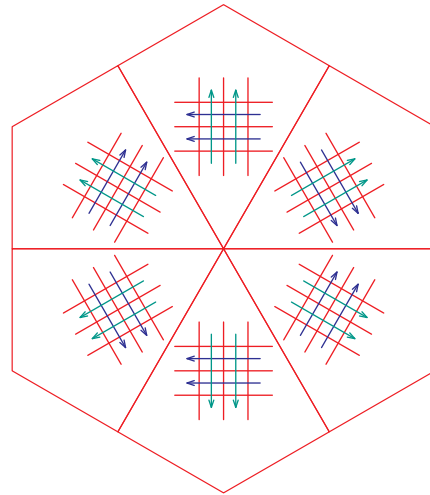
Graphics Input and Control Points



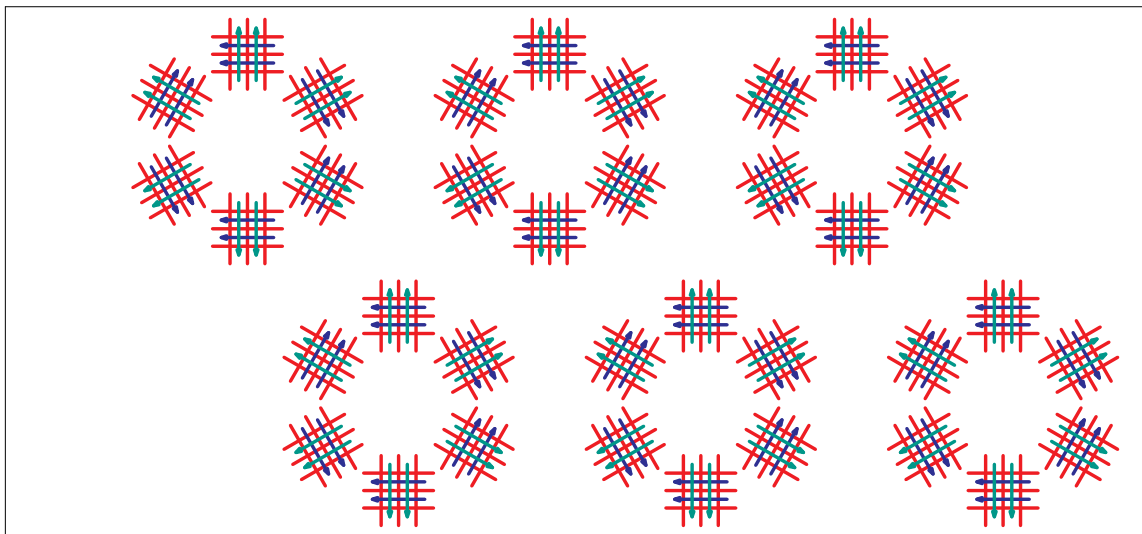
UnitCell



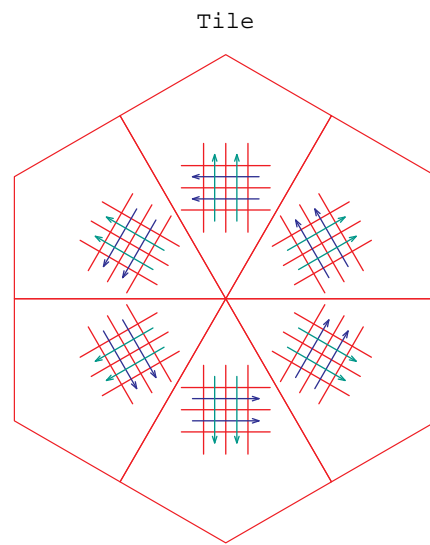
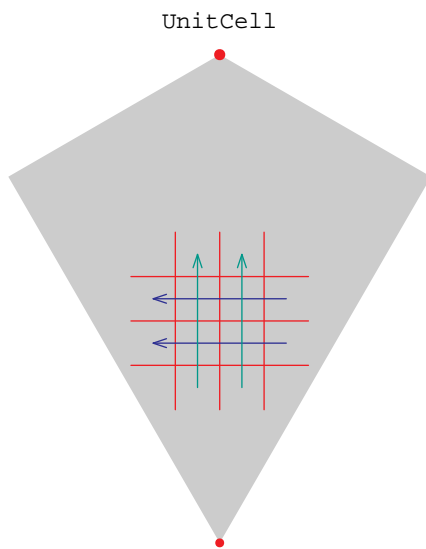
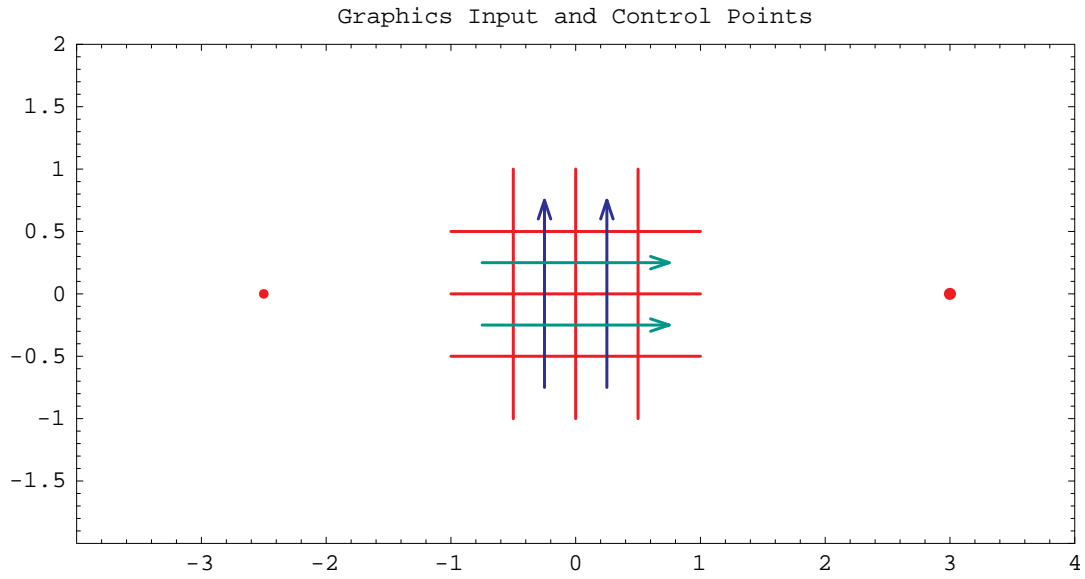
Tile



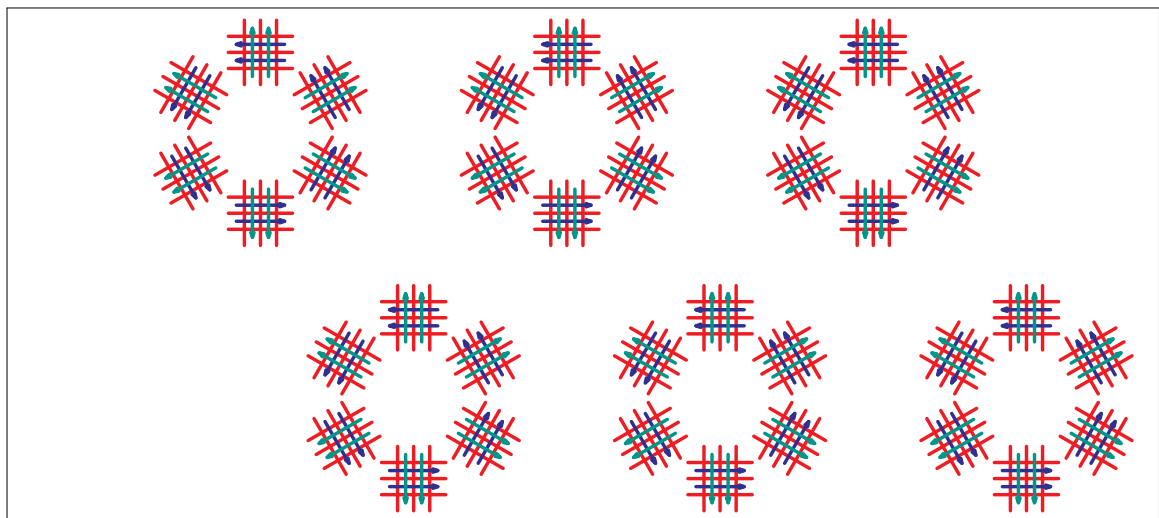
Tiling 2,3



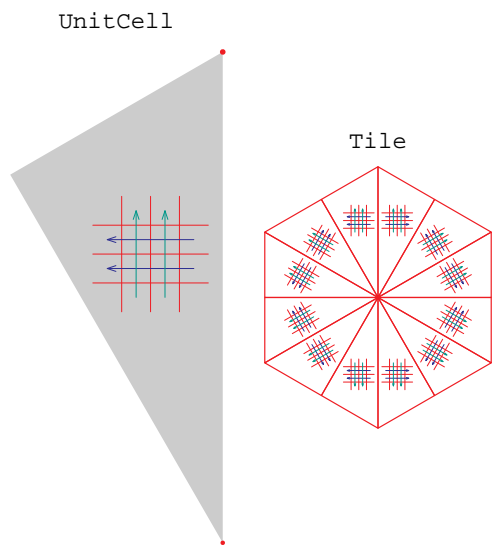
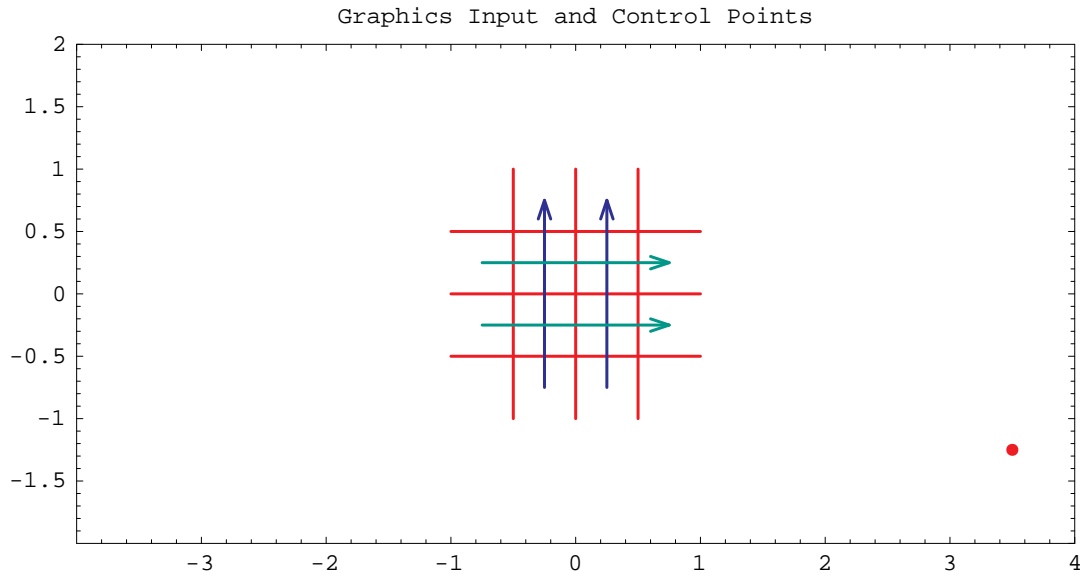
Wallpaper Group: p6



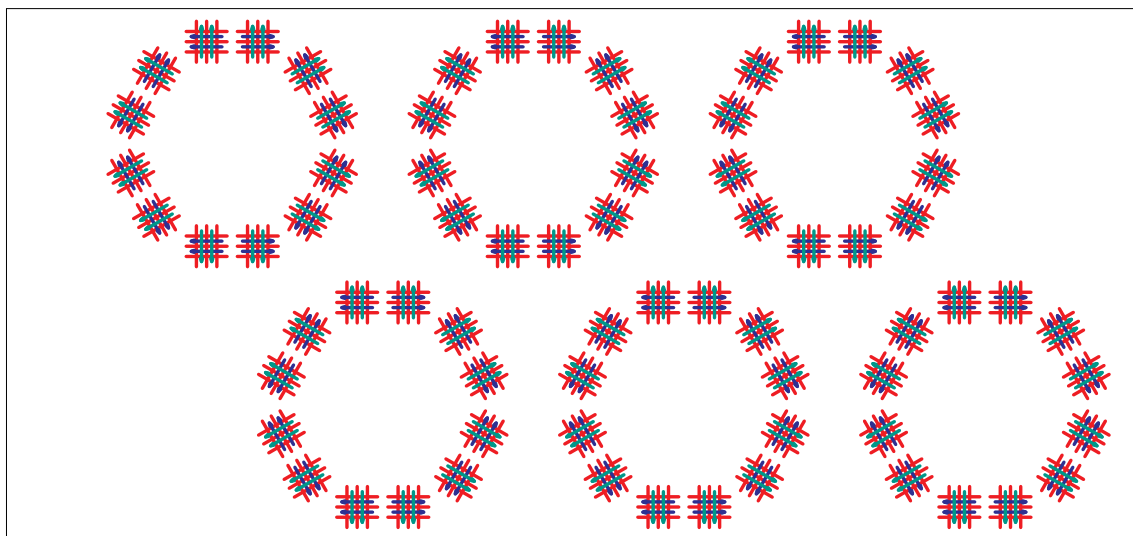
Tiling 2,3



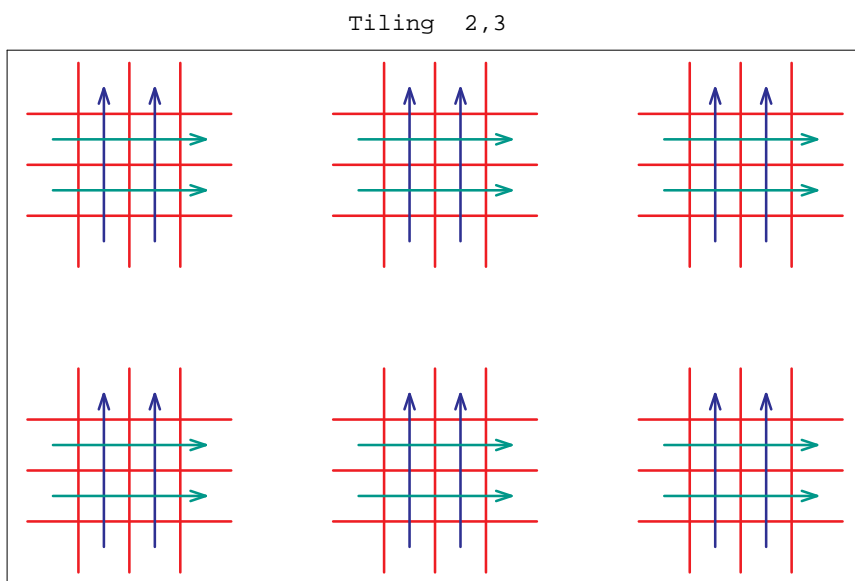
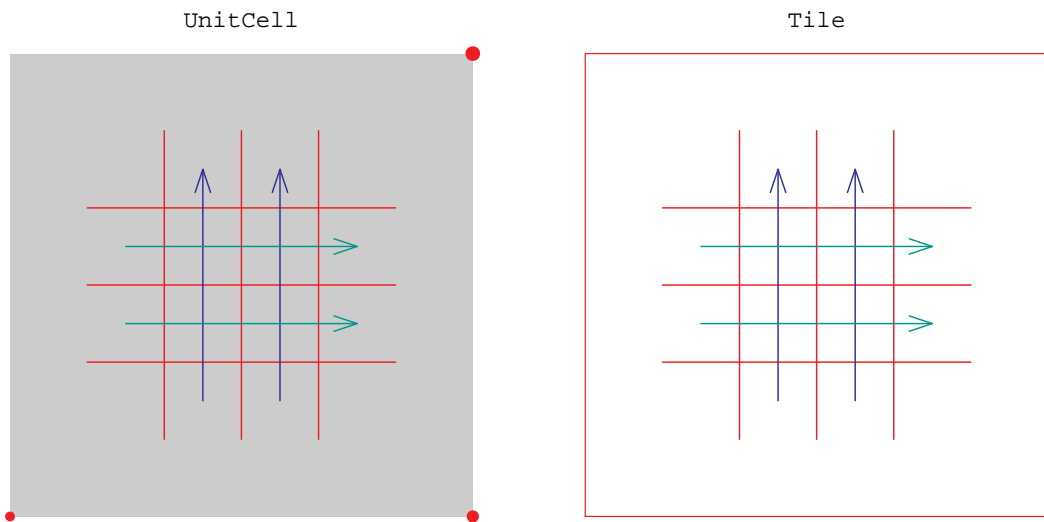
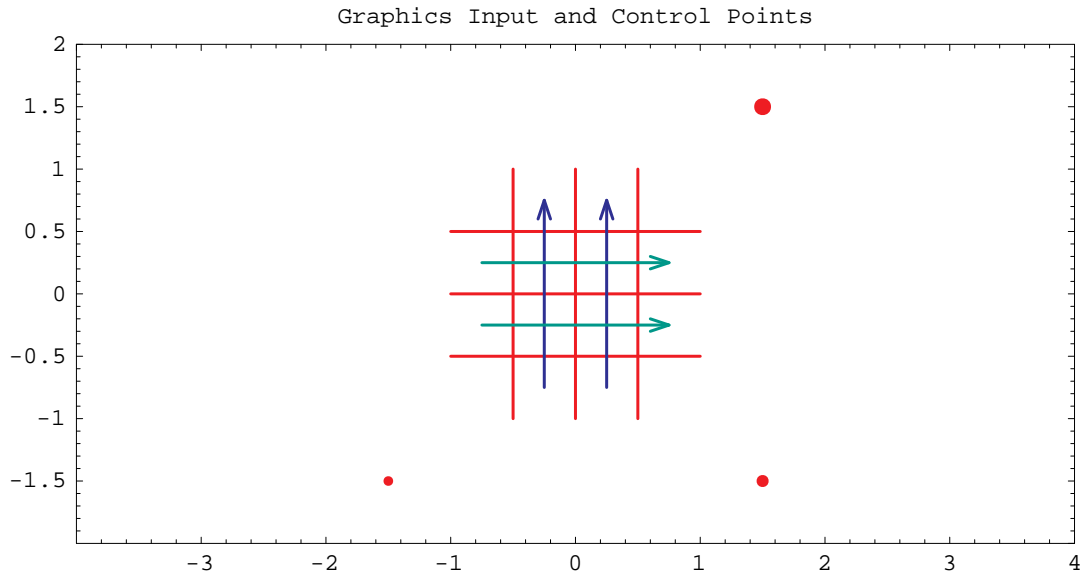
Wallpaper Group: p6m



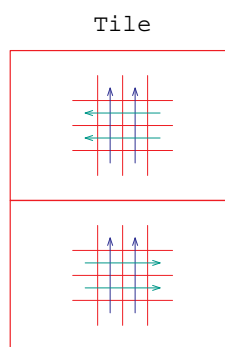
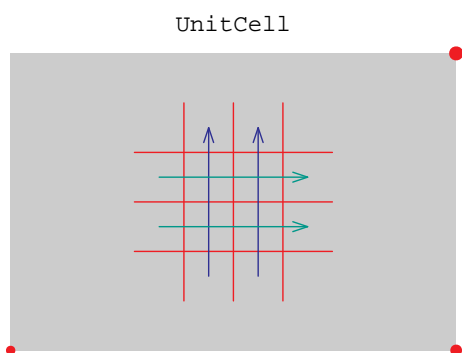
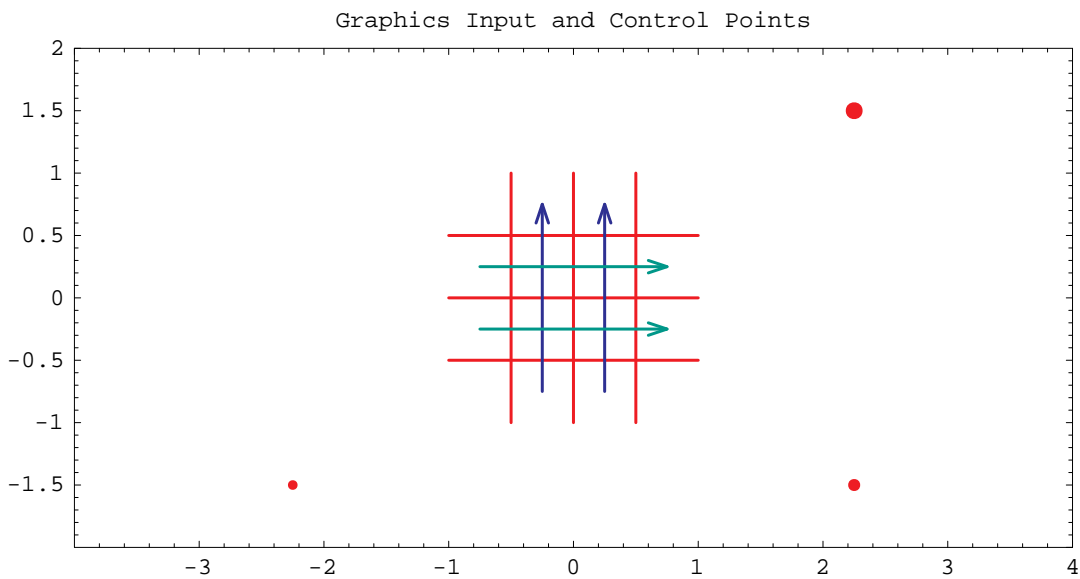
Tiling 2,3



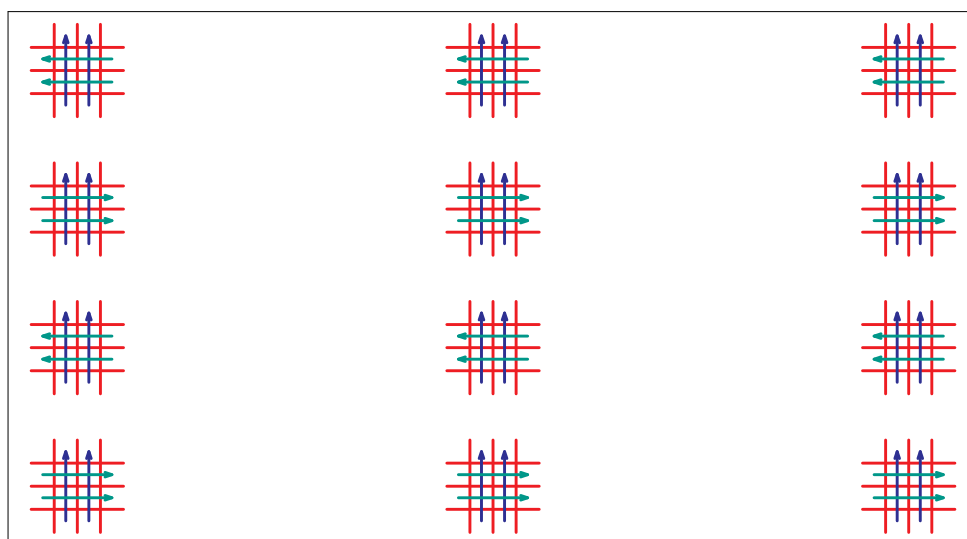
Wallpaper Group: p1



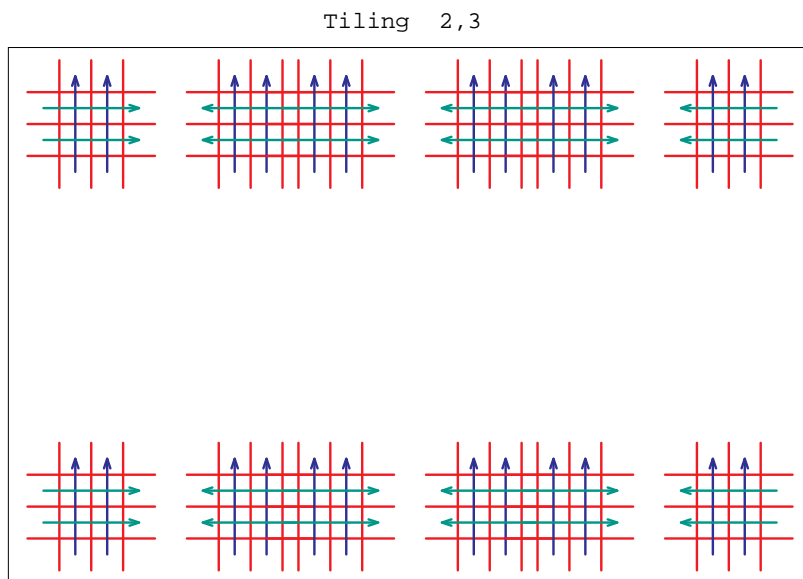
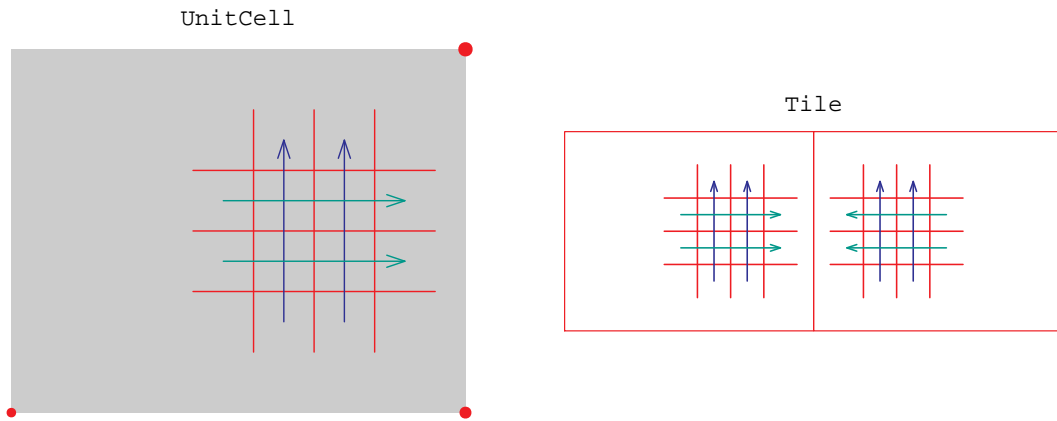
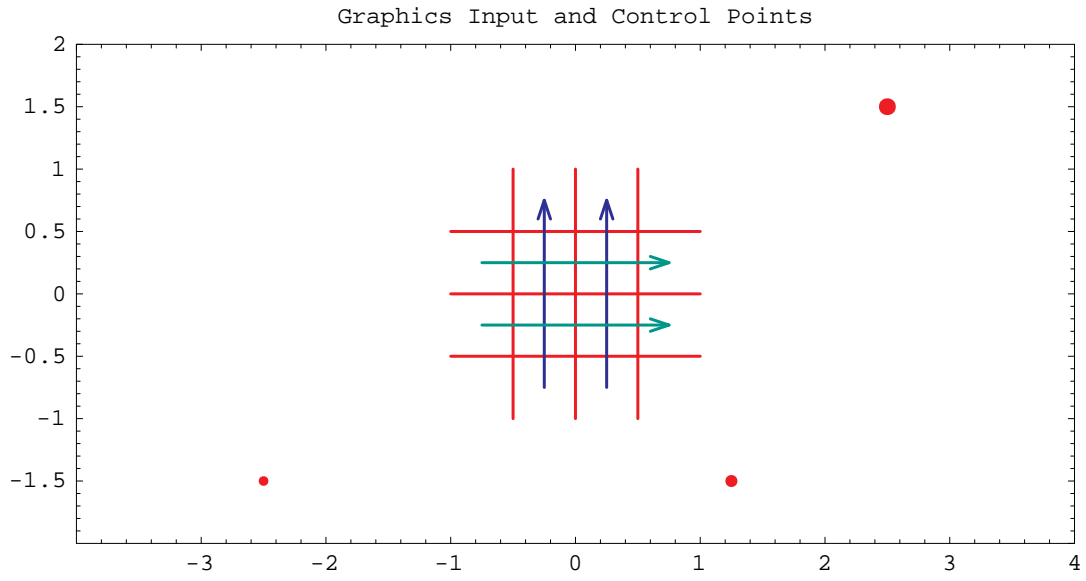
Wallpaper Group: pg



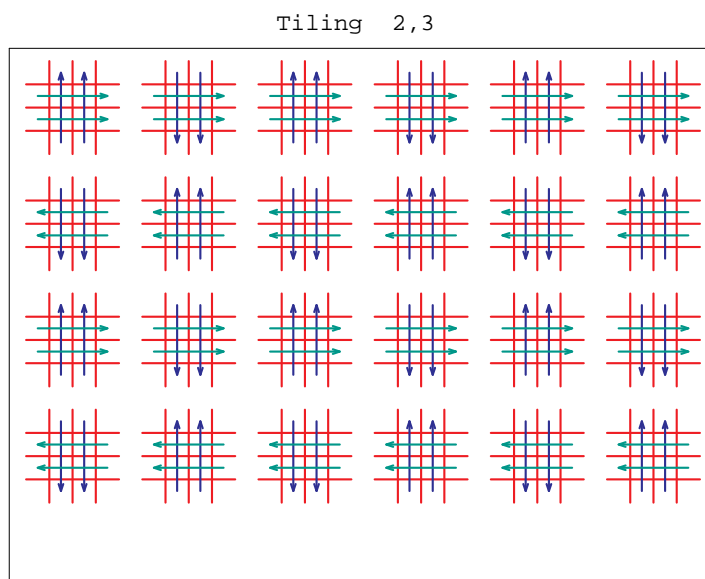
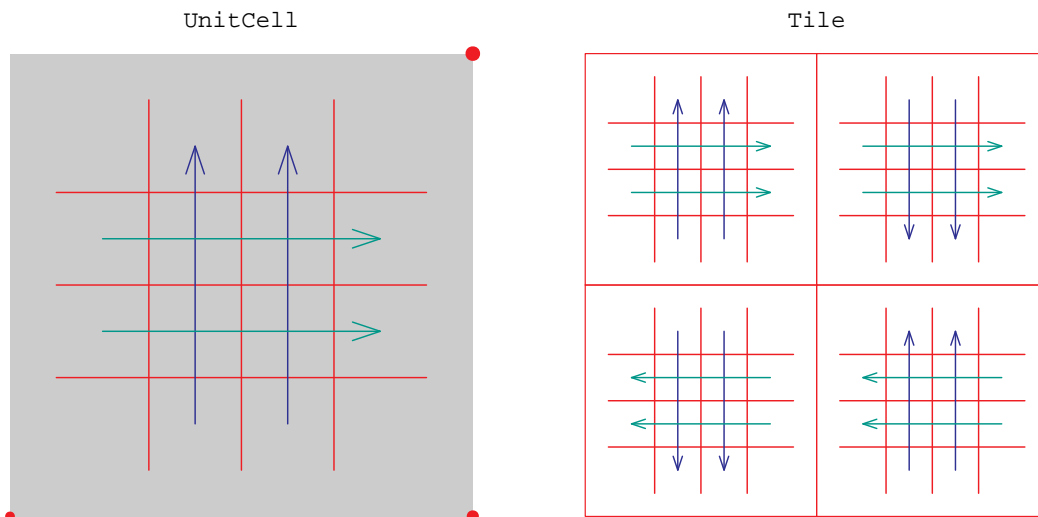
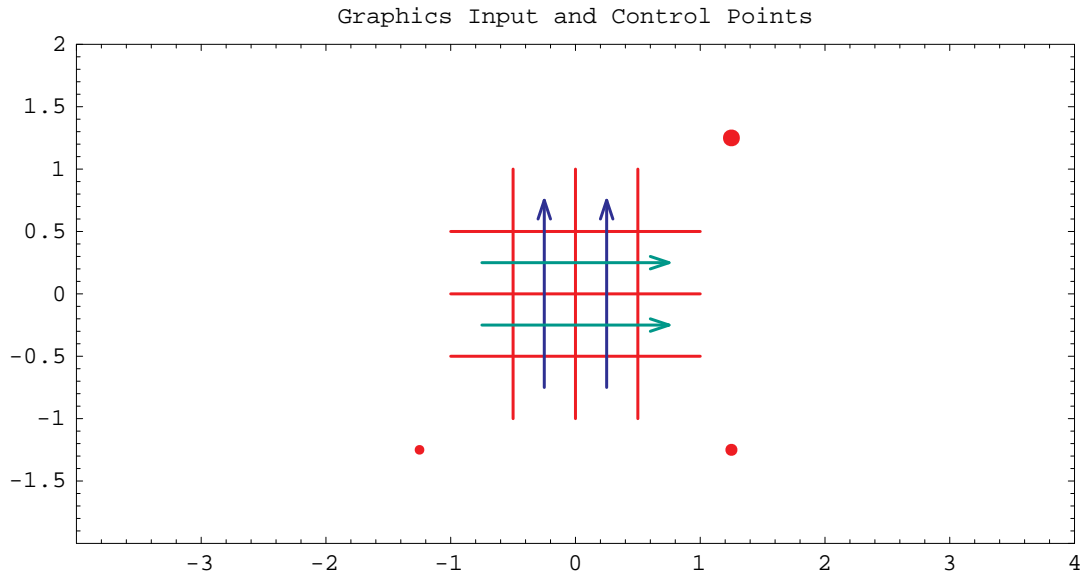
Tiling 2,3



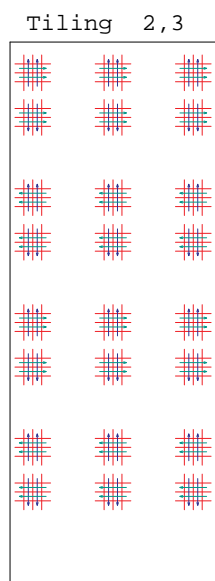
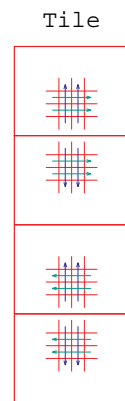
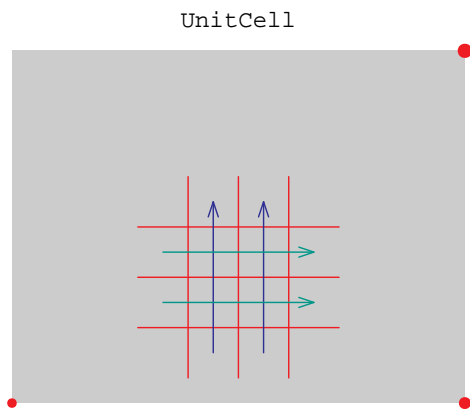
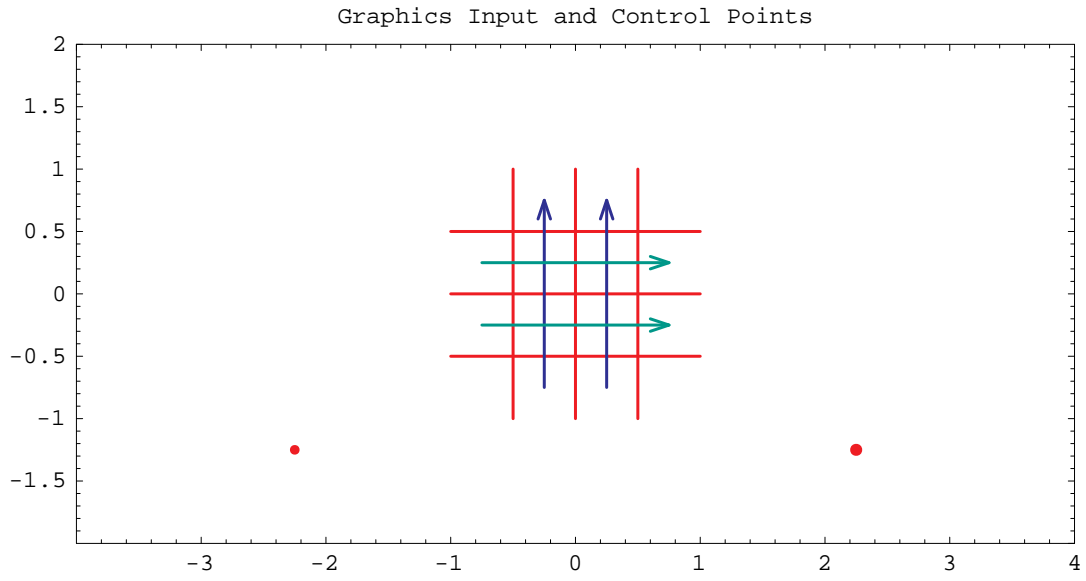
Wallpaper Group: pm



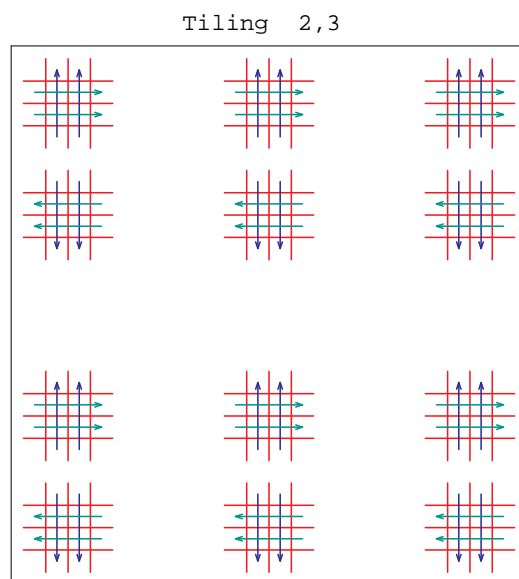
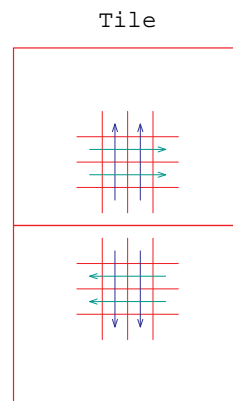
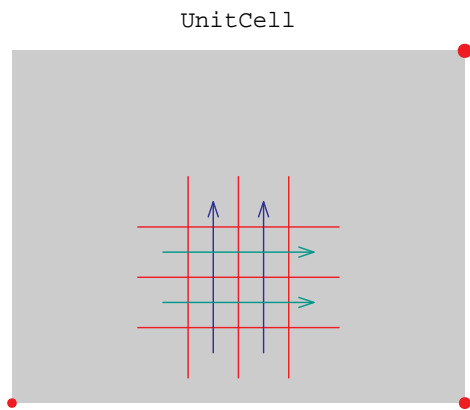
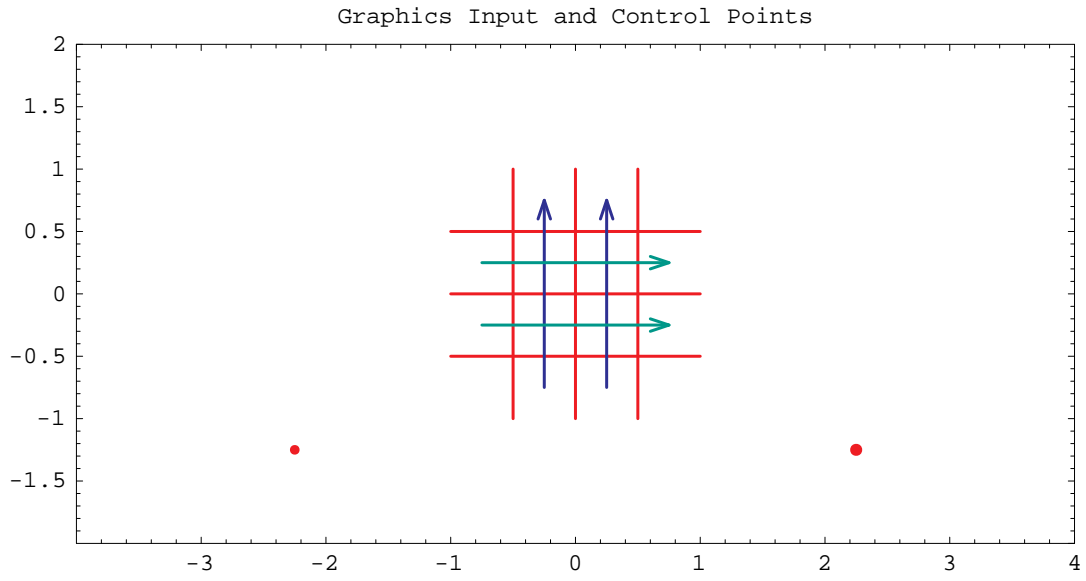
Wallpaper Group: pgg



Wallpaper Group: pmg



Wallpaper Group: p2



April 2001

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